

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
9 January 2003 (09.01.2003)

PCT

(10) International Publication Number
WO 03/003552 A2

(51) International Patent Classification⁷: **H02M 3/00**

(NL). DOMENSINO, Hendrikus, J., J. [NL/NL]; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(21) International Application Number: **PCT/IB02/02364**

(22) International Filing Date: **20 June 2002 (20.06.2002)**

(74) Agent: **DEGUELLE, Wilhelmus, H., G.**; Internationaal Octrooibureau B.V., Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(25) Filing Language: **English**

(81) Designated States (*national*): **CN, JP, KR, US.**

(26) Publication Language: **English**

(30) Priority Data:
01202501.1 **28 June 2001 (28.06.2001)** **EP**

(84) Designated States (*regional*): European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR).

(71) Applicant (*for all designated States except US*): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

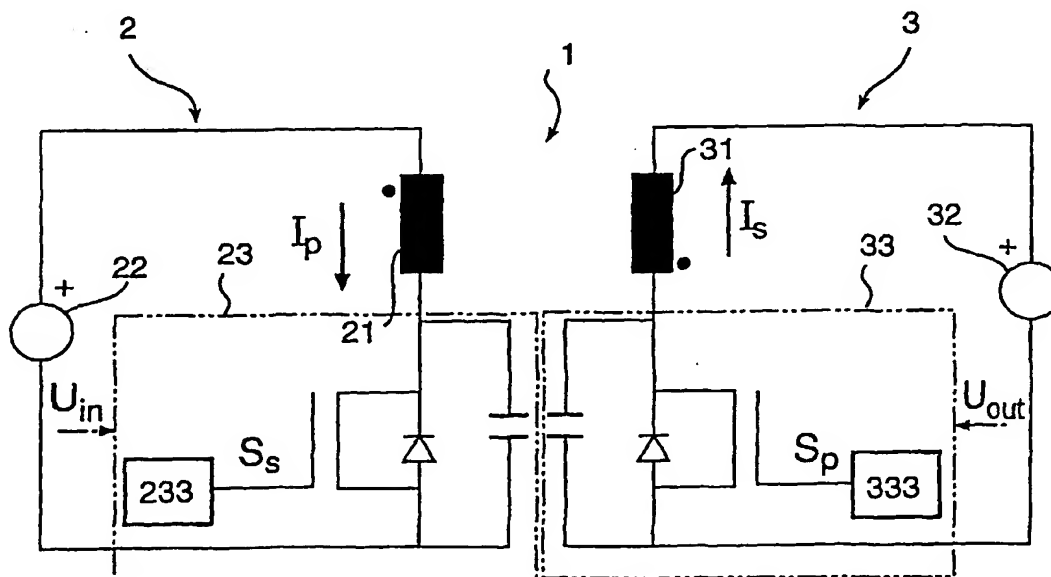
Published:
— *without international search report and to be republished upon receipt of that report*

(72) Inventors; and

(75) Inventors/Applicants (*for US only*): **SMIDT, Pieter, J., M.** [NL/NL]; Prof. Holstlaan 6, NL-5656 AA Eindhoven

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: **BIDIRECTIONAL FLYBACK SWITCH MODE POWER SUPPLY (SMPS)**



(57) Abstract: The present invention relates to a bidirectional flyback switch mode power supply (SMPS), such as a bidirectional flyback converter, and a method for operating it. The SMPS comprises a transformer having primary and secondary windings inductively coupled together, said primary winding being coupled to a voltage input of the converter and said secondary winding being coupled to a voltage output of the converter; a primary control unit comprising a primary switch arranged in series with the primary winding and a secondary control unit comprising a secondary switch arranged in series with the secondary winding. The primary control unit is adapted to compensate input voltage variations, and the secondary control unit is adapted to provide a controlled output power on the secondary side.

WO 03/003552 A2

Bidirectional flyback switch mode power supply (SMPS)

The present invention relates to a bidirectional flyback converter.

The term switch mode converter is conventionally used in the art to describe a circuit which takes a DC or AC input and provides a single or multiple DC output, of the same or opposite polarity and lower or higher voltage. The term Switch Mode Power Supply (SMPS) is a common denomination to describe switch mode regulators and converters. A flyback SMPS circuit transfers energy from an inductor to the output capacitor when a switching element (e.g. a transistor) on the primary side is switched off, whereas a feed-forward SMPS circuit will supply energy to the output capacitor when the switching element is switched on. The principle power train structure of the bidirectional flyback has been known for a long time, and is e.g. described in US 3986097 and EP 0013332.

A bidirectional or reciprocating converter is a converter in which energy is periodically fed back from the secondary side into the primary side. A conventional flyback switched mode power supply, on the other hand, transfers energy in only one direction, i.e. from the input circuit to the output circuit. Such a bidirectional converter is e.g. known from US 5768118. The reciprocating converter uses a secondary switch (in parallel with the secondary rectifier) to allow the secondary current to cross zero and change sign to a negative value as it ramps down. When the switch of the secondary side opens, a negative current appears on the primary winding, and this current reduces the voltage on the switch of the primary side. After the voltage on the switch of the primary side has been reduced to an acceptable minimum level, the switch of the primary side is closed, and the input voltage causes the primary current to resume ramping up.

Accordingly, in a flyback switched mode power converter, the AC wall voltage is filtered and rectified to produce an unregulated DC input voltage. This unregulated DC input voltage is then provided to an input circuit which is connected to the primary winding of a power transformer. An output circuit is connected to the secondary winding of the power transformer, whereby the voltage at the output of the output circuit develops the appropriate DC voltage or DC current levels for an electronic device. The compact size and high efficiency of switched mode power converters are very advantageous in electronic equipment, such as laptop computers, for charging batteries, etc. The different mains voltages

in various countries can then suitably be used to operate the equipment without adaptation or change-over.

Nevertheless, the performance of the bidirectional flyback converters strongly depend on the way the active switches are controlled, and this has been a problem over the years. Attempts to solve this problem have been presented in EP 0336725 and EP 0551212.

Conventional flyback converters are affected by several drawbacks, such as:

- high switching losses;
- isolated feedback is necessary;
- problems to regulate the converter under no-load conditions; and
- bad efficiency due to losses in the output rectifier in case of low output voltages.

These problems are well-known in the art. Individual solutions to the problems have been proposed (see the above-discussed references). Nevertheless, a solution is desirable that overcomes several, and preferably all of the above problems, and/or that solves the problems in a simpler and preferably more effective way.

It is therefore an object of the present invention to provide a bidirectional flyback converter, and a method for controlling same, which alleviates the above-discussed prior art problems.

This object is achieved by means of a converter and a method according to the appended claims.

The invention provides a method of operating a bidirectional flyback switch mode power supply (SMPS), comprising the steps of:

ramping up current in a first direction through a primary winding of a transformer at the primary side of the converter;

opening a primary switch which is in series with said primary winding, thereby driving current through a secondary winding of said transformer at a secondary side of the converter which is inductively coupled to said primary winding;

controlling a secondary switch which is in series with said secondary winding, to permit said secondary winding to pass current in a first direction, and, subsequently, to permit said current to pass through said secondary winding in a second direction which is opposite to said first direction, so as to drive current through said primary winding in a second direction which is opposite to said first direction;

wherein the secondary switch is further controlled in order to provide a

controlled output power on the secondary side; and

wherein the primary switch is further controlled in order to compensate input voltage variations.

5 The invention provides a new control mechanism for a bidirectional flyback, which alleviates the above-discussed disadvantages. Especially, the control mechanism allows a relatively small amount of the circulating energy that results in a good load efficiency of the converter parts. Even in a no-load situation, the power dissipation can be made small at a finite switching frequency.

10 The primary switch is further preferably controlled in order to minimize circulating power.

Preferably, the primary switch comprises a field-effect transistor (FET), preferably a MOSFET, wherein said switch is controlled so as to compensate input voltage variations by keeping the valley of the drain voltage at a relatively constant value.

15 This control can be exercised in a very simple manner and proved to be surprisingly effective. Specifically, this control could limit the circulating power in the converter in a very simple and cost-effective way.

Alternatively, the field-effect transistor (FET) is controlled to compensate input voltage variations by controlling the valley of the drain voltage in dependence on the input voltage.

20 This control can also be exercised in a simple manner and likewise proved to be very effective. Specifically, this control could provide an absolute minimization of the circulating power in the converter over the full input voltage range, as well as total control of the circulating power in the converter.

25 According to another aspect, the invention relates to a bidirectional flyback switch mode power supply (SMPS) comprising:

a transformer having primary and secondary windings inductively coupled together, said primary winding being coupled to a voltage input of the converter and said secondary winding being coupled to a voltage output of the converter;

30 a primary control unit comprising a primary switch arranged in series with the primary winding;

a secondary control unit comprising a secondary switch arranged in series with the secondary winding;

characterized in that the primary control unit is adapted to compensate input

voltage variations, and in that the secondary control unit is adapted to provide a controlled output power on the secondary side.

The invention thus provides the same control mechanism as that discussed above, with the same advantages.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiment(s) described hereinafter.

In the drawings:

Fig 1 is a schematic illustration of a general flyback converter according to an embodiment of the invention;

Fig 2 is a schematic illustration of an embodiment of the control unit in the converter in fig 1;

Fig 3 is a diagram illustrating the waveforms and modes of the voltages and currents of the primary and secondary side, respectively, during operation of the bidirectional flyback converter in fig 1;

Fig 4 illustrates primary-side peak currents for conventional bidirectional flyback converters; and

Fig 5 illustrates the circulating energy in conventional bidirectional flyback converters.

Fig 6 illustrates a simple detection circuit to be used in an embodiment of the invention.

The scope of the applicability of the present invention will become apparent from the detailed description given hereinafter. However, it should be understood that the detailed description and specific examples, indicating preferred embodiments of the invention, are given by way of illustration only, since, within the spirit and scope of the invention, various changes and modifications will become apparent to those skilled in the art.

A bidirectional flyback switched mode converter according to an embodiment of the invention is schematically illustrated in Fig 1. Said Figure shows is a power transformer 1 with a primary side 2 and a secondary side 3. The primary side has a primary winding comprising an inductor 21, and the secondary side has a secondary winding

comprising an inductor 31. Furthermore, the primary side comprises an input circuit 22, where a DC input voltage, U_{IN} , is connected/applied ? to one end of the inductor 21. The other end of the inductor 21 is connected to a primary control unit. The control unit preferably comprises a transistor, most preferably a metal-oxide-semiconductor field-effect transistor (MOSFET), such as an N-type depletion-mode MOSFET (NMOS), and means for controlling the transistor. The control unit will be discussed in more detail hereinbelow .

U_{IN} could typically be provided by an AC wall voltage, which is passed through a rectifier. U_{IN} typically ranges from 50 to 400 volts. I_P is the current from inductor 21 having the orientation shown in fig 1.

In the secondary side, an output circuit 32 is provided to one end of the inductor 31, providing a DC output voltage, U_{OUT} . The other end of the inductor is connected to a secondary control unit 33. The secondary control unit preferably also comprises a transistor, most preferably a metal-oxide-semiconductor field-effect transistor (MOSFET), such as an N-type depletion-mode MOSFET (NMOS), and means for controlling the transistor.

Accordingly, the converter uses two controllers: one controller for the primary switch, and one controller for the secondary switch. Each controller controls the on-off switching of the associated switch.

An embodiment of the control units 23, 33 is illustrated in fig 2. Each of the control units preferably comprises a switch 231, 331 with an anti-parallel diode 232, 332 and control circuitry 233, 333 that can be either discrete or integrated. The control part exhibits input channels to receive information from the converter stage. The driver of the control part generates the drive signal for the active switch. The switch can be realized by means of any available switching device. Preferably a MOSFET is used, as this allows the internal body to represent the anti-parallel diode, thereby saving one additional component. In this case, the drain of the MOSFET or any other transistor is connected to the inductor. Furthermore, a parasitic capacitance (not shown) could be arranged so as to lie between the drain of the transistor and the ground.

The switch is controlled from the control part by means of a control voltage, S_P and S_S for the primary and secondary side, respectively. If the switch is a transistor, the control voltage is connected/applied ? to the gate of the transistor.

Fig 3 illustrates the principles on which the behavior of the bidirectional flyback converter is based. In order to keep explanations simple, the flyback is assumed to be ideal. This means that no parasitic elements are included (leakage and stray inductances as well as parasitic capacitances). The dead time during which the soft transition occurs (zero voltage switching, ZVS) will also be neglected during the explanation.

There are two modes of operation : conduction mode I at the primary side and conduction mode II at the secondary side. Both modes can be divided into two sub-modes. During the first period A of each mode, energy is delivered to the relevant side, while during the second period B, energy that is to be delivered to the other side is stored in the transformer.

Fig 3 illustrates the currents and voltages of the bidirectional energy flyback switched mode power supply. Next, a description is given of one switching cycle, from time t_1 until time t_5 , as controlled by the above-discussed control units. At time t_1 , the primary switch is turned on. During the time from t_1 until t_3 , i.e. in mode I, I_p ramps upward due to the DC voltage, U_{IN} , which is present across inductor 21. Furthermore, during the time from t_1 until t_3 , there is no secondary current, I_s . This is attributable to the fact that the secondary switch is turned off.

Although I_p is initially negative up to the time t_2 , the positive primary current, I_p , stores energy in the magnetic field of transformer 1 during the time from t_1 until t_3 .

After sufficient time has elapsed, S_p turns off the switch in the primary control unit 23 at time t_3 . After S_p has turned off the primary switch, S_s turns on the secondary switch in the secondary control unit 33 after a short delay not shown in fig 3. Once the primary switch has been turned off, a positive secondary current, I_s , is provided. During time t_3 to time t_5 , i.e. during mode II, I_s ramps downward, thereby transferring energy stored in the magnetic field of transformer 1 to the output, which completes the transfer of energy from the input circuit to the output circuit.

Once the energy stored in the magnetic field of transformer 1 has been transferred, some energy is transferred back to transformer 1. The energy transferred back is used to discharge capacitor 114 before transistor 110 is switched on. As shown in fig 3, from time t_4 until time t_5 , I_s is negative, thus causing energy to be stored in the magnetic field of transformer 1.

At time t_5 , the secondary switch is turned off. Thus, there is no secondary current, and I_s is made equal to zero from time t_5 until time t_3 on the next cycle.

Subsequently, the primary switch is once again switched on, thereby starting another switching cycle. Primary current I_p , at time t_5 , has an initial value which is negative.

During each sub-mode A, the current can flow either through a rectifier diode - in the case of a MOSFET the internal body can be used - or through the switch, if a bidirectional switch is used and activated. The switch can be switched on during the complete period A under zero voltage condition or at reduced switching losses.

As discussed above, the secondary current, I_s , is allowed to ramp down to negative currents before the switch of the secondary side opens. When the switch of the secondary side opens, a negative current appears on the primary winding, and this current reduces the voltage on the switch of the primary side. After the voltage on the switch of the primary side has been reduced to an acceptable minimum level, the switch of the primary side is closed, and the primary current begins to ramp up again.

According to the invention, the problems with the above-discussed prior art solutions are solved by an appropriate control of the active switches.

A dead time between the control signals for the two switches together with the correct direction of the transformer currents results in zero voltage switching or reduced switching losses. Thus, the switching losses are eliminated or at least reduced. This makes it possible to use a higher switching frequency and thus allows more efficient and/or smaller/cheaper designs.

The bidirectional flyback converter provides a power flow from the primary to the secondary side but also in the opposite direction, i.e. from the secondary to the primary side. Thus, the control at the secondary side can reflect excessive power back to the primary side. This makes it possible to regulate output voltage or output current at the secondary side. Thus, an isolating feedback path can be avoided. The main task of the primary-side control is to supply enough energy to the secondary side.

Under steady-state conditions, a certain minimum amount of energy is reflected back to the primary side. Under no-load conditions the secondary-side controller will reflect the complete energy transferred from the primary side, minus the energy dissipated during one cycle through the transformer. The energy transfer from and to the secondary side needs a certain time span. Thus, the converter is preferably regulated at a finite frequency.

It is also possible to close the primary switch in one of the later valleys of the switch voltage. In this way, efficiency can be further improved at lower loads.

Referring again to figs. 1 and 2, the input signals to the control unit at the secondary side are preferably the output voltage and/or the output current because these are easily accessible values. These values are compared with the corresponding reference values. The resulting error signal is used to set the amount of reflected output power. The amount of output power could be determined either by controlling the negative peak current or by controlling the forward conduction time of the secondary switch. The derivation of the control value from the error signal could be done by conventional techniques, such as an error amplifier with PI-characteristics, etc. The output current at the secondary side is substantially a DC current, and is measured after the output smoothing resistance. The secondary current, I_s , can be monitored by a current sensor, such as a resistor, connected in series with the secondary winding 31, or can be estimated by monitoring the elapsed time from time t_3 when the secondary switch 331 is first turned on. Similarly, the output voltage could e.g. be detected by a voltmeter or the like.

One additional signal is preferably provided, indicating whether the diode is conducting. This signal allows to switch-on the active switch under zero voltage conditions. The signal can be derived either from the voltage across the parallel combination of active switch and anti-parallel diode or by the current through this combination. Thus, the signal can be derived by conventional methods. However, if the switch should be switched on at reduced switching losses, i.e. no real zero voltage condition, it is sufficient to monitor the voltage across the switch and switch it on when an acceptable level is reached.

The primary control part is preferably also provided with a signal indicating whether the corresponding anti-parallel diode is conducting or with a signal indicating the presence of a valley in the switch voltage. Such signals can be derived using conventional methods. The signals allows to switch-on the primary switch under zero voltage conditions or at reduced switching losses. In addition, the primary control should preferably be provided with a signal that ensures an energy transfer that is high enough for all load situations. Several control variables are further possible, e.g. fixed peak current, fixed switching frequency, fixed on-time, etc., as is known in the art. However, as will be discussed in the hereinafter with reference to figs. 4 and 5, this approach is not useful in applications that have to deal with a substantial input voltage variation.

At present, nearly all applications using AC-DC conversion have to be designed for a universal mains input. This results in an input voltage range of more or less 50-400 V for the flyback converter. Since the on-time solution has to be adapted to ensure a high enough energy transfer also for the lowest input values, extremely high currents result

for the higher input values. Fig 5 depicts this situation. The currents are normalized to the minimum absolute value of the negative peak current at the primary side. The negative current is necessary to obtain ZVS for the primary-side MOSFET. In fig 4, thick lines indicate positive peak values and thin lines indicate negative peak values, whereas dotted lines indicates minimum input voltage, solid lines indicate nominal input voltage and dashed lines indicates maximum input voltage. As is clearly seen in Fig 4, in the worst case, the normalized negative peak current is nearly 30 times higher, leading to high current ratings, rms losses and to a large amount of circulating energy.

The corresponding amount of circulating energy, normalized to the maximum output power, in dependence on the load for different input voltages is shown in Fig 5. In fig 5, dotted lines indicate the circulating energy for minimum input voltages, solid lines indicate the circulating energy for nominal input voltages and dashed lines indicate the circulating energy for maximum input voltages. While for low input voltages the amount of circulating energy is relatively small for all load situations, the amount of circulating energy ramps up to five times (in this example) the maximum output power. This clearly demonstrates the drawback of such a control system. In the case of wide input ranges, this method is not acceptable.

According to the invention, said drawback is obviated by controlling the control units so as to tackle the input voltage variation at the primary side, while load regulation/variation ? is tackled at the secondary side. If the primary-side control unit comprises a MOSFET, the MOSFET could, to this end, be controlled in such a way that the valley of the drain voltage is kept at a constant value. Alternatively, the valley of the drain voltage could be controlled in dependence on the input voltage. The input voltage, V_{IN} , could, to this end, be monitored through a voltmeter or an auxiliary winding of transformer 1. The drain voltage valley could be detected with a conventional sample-and-hold technique or with a simple detection circuit comprising the auxiliary winding (aux), as illustrated in fig 6. In this case, the valley on the drain is a peak. By closing the transistor T when S_s is high, detection of the wrong value is precluded. The diode D and the capacitor C form a simple peak rectifier. A resistance R_{dis} can be added for improved dynamic behavior of the converter.

Once the valley value (i.e. the peak value in the above discussed circuit) is known, a simple PI error amplifier could be used for controlling the device accordingly.

In order to further optimize the system, the setpoint (i.e. the desired value) of the valley could be made a function of V_i . For example, V_{set} could be proportional to V_i .

A converter as described above tackles the input voltage variation at the primary side while load regulation is tackled at the secondary side. As a result, the amount of circulating energy in the converter is minimized to the level indicated by the dotted line in fig 5 for all input voltages. This leads to a much more efficient and cost-effective converter, due to the elimination of the isolated feedback, higher efficiency, lower electromagnetic interference (EMI), smaller size, etc.

The converter according to the invention could be used both for AC-DC and DC-DC power conversion. It is especially suitable for all applications in the lower power range, i.e. up to a few hundreds Watts.

The primary and secondary control units may operate independently of one another, or one controller may be dependent on the signals generated by the other controller. If the two controllers operate independently of one another, each controller must monitor the states of the power supply in order to determine the appropriate switching times. If the two controllers interact with each other, one controller operates a master controller, and one operates as a slave controller.

Furthermore, other variations and alternatives are possible as well. For example, as will be obvious to those of ordinary skill in the art, other circuit elements can be added to, or be used as substitutes in, the specific circuit embodiments discussed above, such as other types of switching means, other types of control units, etc. Furthermore, the invention may be used in a large range of different applications. Such modifications and other closely related modifications must be considered to be within the scope of the invention as defined by the appended claims.

CLAIMS:

1. A method of operating a bidirectional flyback switch mode power supply (SMPS), comprising the steps of:

ramping up current in a first direction through a primary winding of a transformer at the primary side of the converter;

5 opening a primary switch which is in series with said primary winding, thereby driving current through a secondary winding of said transformer at a secondary side of the converter which is inductively coupled to said primary winding;

controlling a secondary switch which is in series with said secondary winding, to permit said secondary winding to pass current in a first direction, and, subsequently, to

10 permit said current to pass through said secondary winding in a second direction which is opposite to said first direction, so as to drive current through said primary winding in a second direction which is opposite to said first direction;

wherein the secondary switch is further controlled in order to provide a controlled output power on the secondary side; and

15 wherein the primary switch is further controlled in order to compensate input voltage variations.

2. A method according to claim 1, wherein the primary switch is further controlled in order to minimize circulating power.

20 3. A method according to claim 1 or 2, wherein the primary switch comprises a field-effect transistor (FET), preferably a MOSFET, wherein said switch is controlled so as to compensate input voltage variations by keeping the valley of the drain voltage at a relatively constant value.

25 4. A method according to claim 1 or 2, wherein the primary switch comprises a field-effect transistor (FET), preferably a MOSFET, wherein said switch is controlled so as to compensate input voltage variations by controlling the valley of the drain voltage in dependence on the input voltage.

5. A method according to claim 4, wherein the field-effect transistor is feed-forward controlled.

5 6. A method according to any one of the preceding claims, wherein the primary and secondary switches are further controlled to provide a dead time between deactivation of one and activation of the other.

7. A method according to any one of the preceding claims, wherein the control of
10 the secondary switch is based on input signals representative of the output voltage and/or the output current.

8. A method according to any one of the preceding claims, wherein the control of the primary switch is based on an input signal representative of the input voltage.

15 9. A bidirectional flyback switch mode power supply (SMPS) comprising:
a transformer having primary and secondary windings inductively coupled together, said primary winding being coupled to a voltage input of the converter and said secondary winding being coupled to a voltage output of the converter;
20 a primary control unit comprising a primary switch arranged in series with the primary winding;
a secondary control unit comprising a secondary switch arranged in series with the secondary winding;
characterized in that the primary control unit is adapted to compensate input
25 voltage variations, and in that the secondary control unit is adapted to provide a controlled output power on the secondary side.

10. A power supply according to claim 9, wherein the primary control unit comprises a field-effect transistor (FET), preferably a MOSFET, having a conduction
30 channel and a control terminal.

11. A power supply according to claim 10, wherein the primary control unit is adapted to control the field-effect transistors (FET) so as to compensate input voltage variations by keeping the valley of the drain voltage at a relatively constant value.

12. A power supply according to claim 10, wherein the primary control unit is adapted to control the field-effect transistors (FET) so as to compensate input voltage variations by controlling the valley of the drain voltage in dependence on the input voltage.

5

13. A power supply according to any one of the claims 9-12, wherein the power supply is a switch mode power converter.

14. A power supply according to any one of the claims 9-13, wherein the
10 secondary control unit is provided with input signals representative of the output voltage and/or the output current.

15. A power supply according to claim 14, wherein the secondary control unit further comprises a diode, and wherein the secondary control unit is further provided with an
15 input signal indicating whether the diode is conducting.

16. A power supply according to any one of the claims 9-15, wherein the primary control unit is provided with an input signals representative of the input voltage.

20 17. A power supply according to any one of the claims 9-16, wherein the primary control unit further comprises a diode, and wherein the primary control unit is provided with an input signal indicating whether the diode is conducting.

1/4

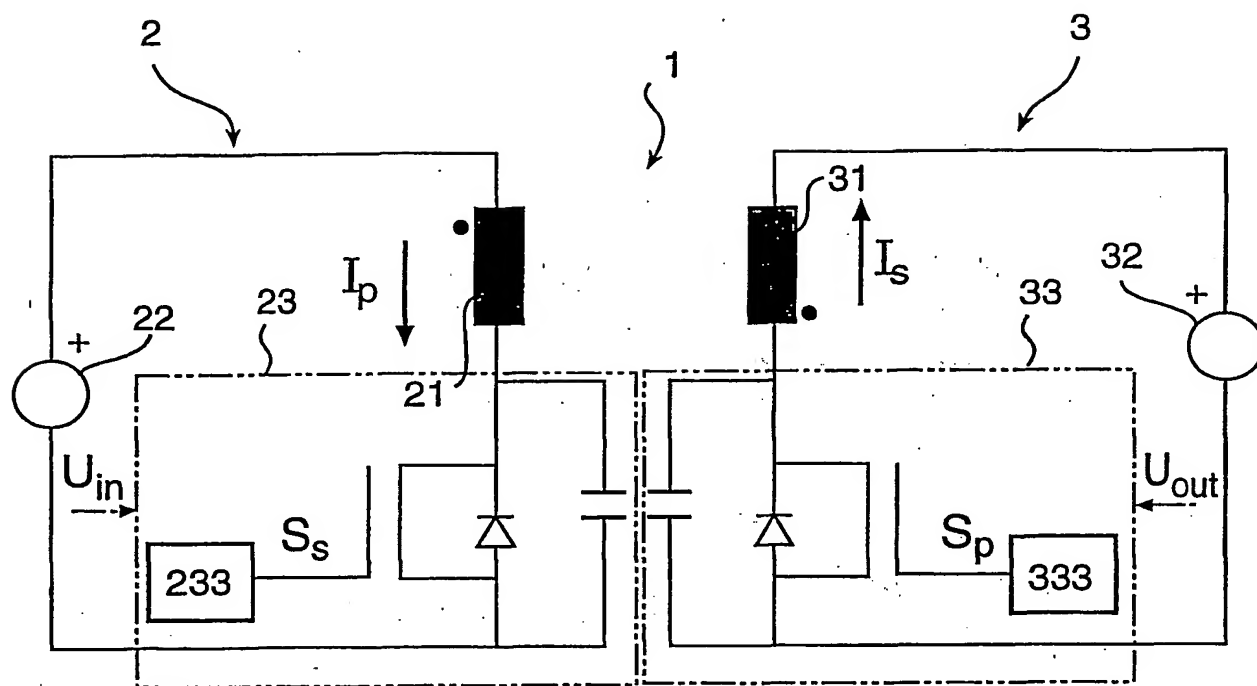


Fig. 1

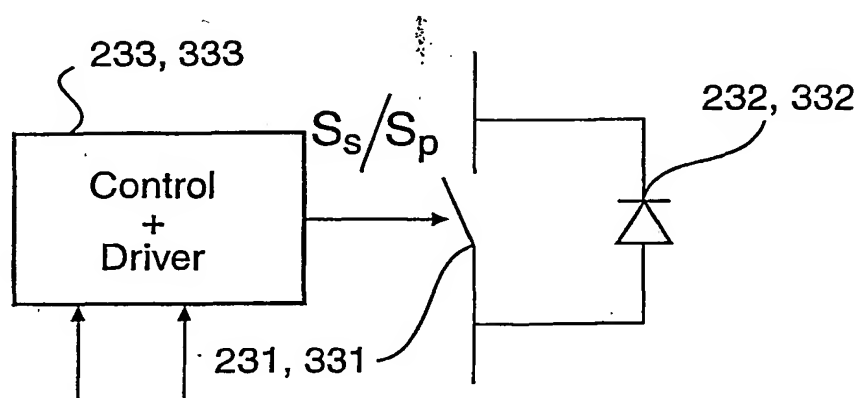


Fig. 2

2/4

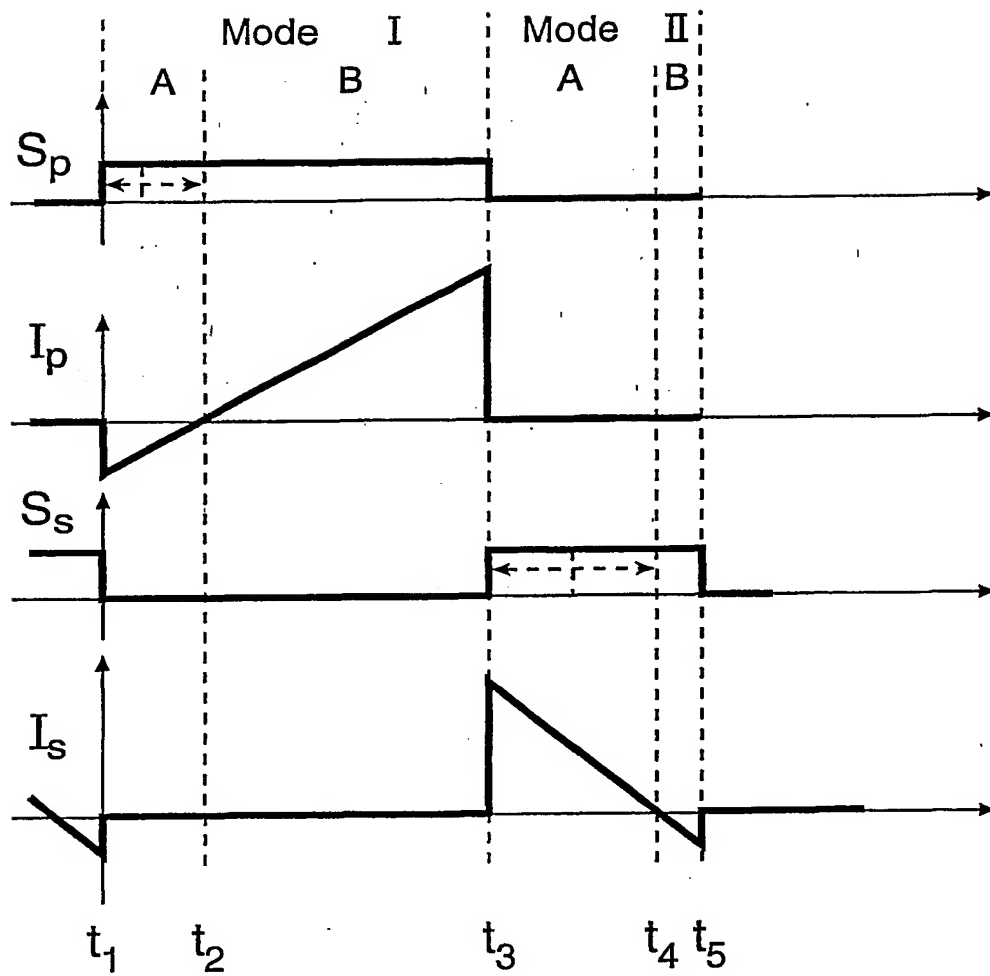


Fig. 3

3/4

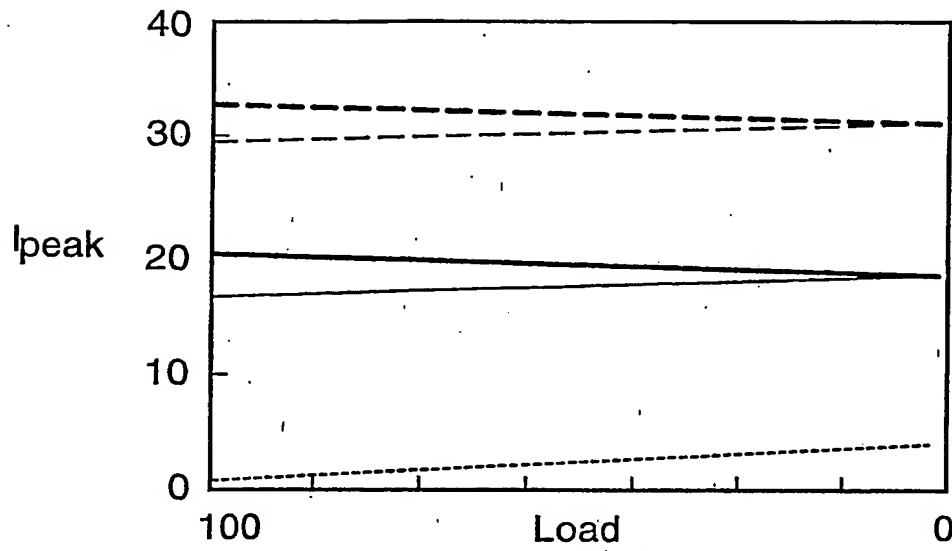


Fig. 4

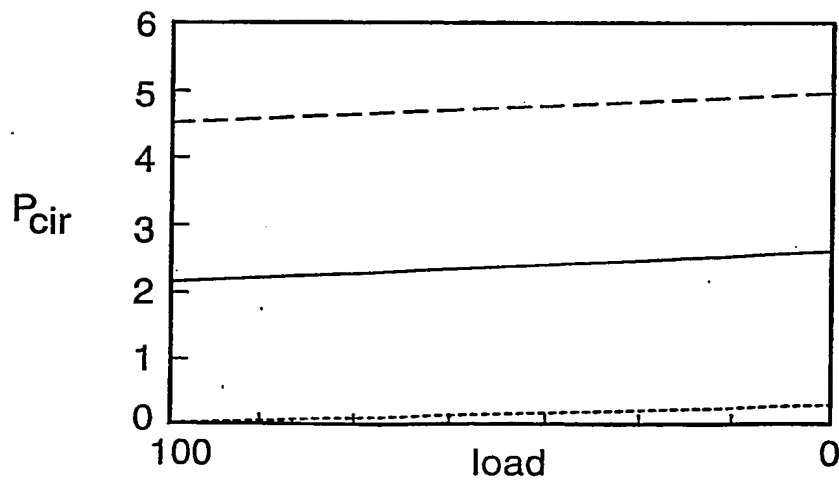


Fig. 5

4/4

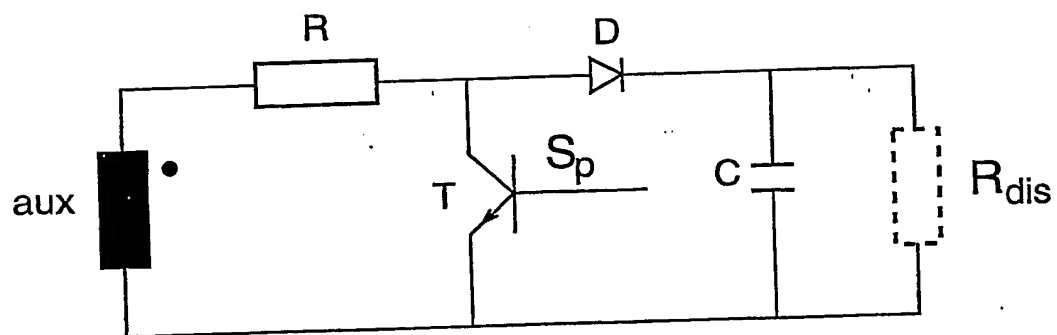


Fig. 6

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
9 January 2003 (09.01.2003)

PCT

(10) International Publication Number
WO 03/003552 A3

(51) International Patent Classification⁷: **H02M 3/335**

(NL). DOMENSINO, Hendrikus, J., J. [NL/NL]; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(21) International Application Number: **PCT/IB02/02364**

(74) Agent: **DEGUELLE, Wilhelmus, H., G.**; Internationaal Octrooibureau B.V., Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(22) International Filing Date: **20 June 2002 (20.06.2002)**

(25) Filing Language:

English

(81) Designated States (*national*): CN, JP, KR, US.

(26) Publication Language:

English

(84) Designated States (*regional*): European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR).

(30) Priority Data:

01202501.1

28 June 2001 (28.06.2001) EP

Published:

— with international search report

(71) Applicant (*for all designated States except US*): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(88) Date of publication of the international search report:
5 June 2003

(72) Inventors; and

(75) Inventors/Applicants (*for US only*): **SMIDT, Pieter, J., M.** [NL/NL]; Prof. Holstlaan 6, NL-5656 AA Eindhoven

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: **BIDIRECTIONAL FLYBACK SWITCH MODE POWER SUPPLY (SMPS)**

(57) Abstract: The present invention relates to a bidirectional flyback switch mode power supply (SMPS), such as a bidirectional flyback converter, and a method for operating it. The SMPS comprises a transformer having primary and secondary windings inductively coupled together, said primary winding being coupled to a voltage input of the converter and said secondary winding being coupled to a voltage output of the converter; a primary control unit comprising a primary switch arranged in series with the primary winding and a secondary control unit comprising a secondary switch arranged in series with the secondary winding. The primary control unit is adapted to compensate input voltage variations, and the secondary control unit is adapted to provide a controlled output power on the secondary side.

WO 03/003552 A3

INTERNATIONAL SEARCH REPORT

Application No
715 02/02364A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H02M3/335

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 084 784 A (THOMAS DÜRBAUM) 4 July 2000 (2000-07-04) abstract figures 1,2 column 1, line 5 - line 24 column 2, line 47 - line 51 column 3, line 12 - line 65 ---	1,2, 6-10, 13-17
X	US 5 745 351 A (CHRISTOPHE TAURAND) 28 April 1998 (1998-04-28) abstract figures 10,11 column 12, line 1 - line 24 ----- -/-	1,2

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

* Special categories of cited documents:

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

- *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- *&* document member of the same patent family

Date of the actual completion of the international search

19 February 2003

Date of mailing of the international search report

27/02/2003

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Lund, M

INTERNATIONAL SEARCH REPORT

International Application No.

PCT/IB 02/02364

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	AT 399 432 B (SIEMENS AKTIENGESELLSCHAFT ÖSTERREICH) 26 May 1995 (1995-05-26) abstract page 4, line 30 - line 35 figure 1	1,2
A	US 5 220 492 A (DANIEL RUBIN ET AL.) 15 June 1993 (1993-06-15) abstract column 4, line 18 - line 24 column 6, line 30 - line 48	1
A	US 5 109 326 A (HUBERT C. MARTIN) 28 April 1992 (1992-04-28) abstract figure 1 column 2, line 32 - line 40	1

INTERNATIONAL SEARCH REPORT

International application No.
PCT/IB 02/02364

Box I Observations where certain claims were found unsearchable (Continuation of item 1 of first sheet)

This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. ☒ Claims Nos.: 3-5 11 12
because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically:
see FURTHER INFORMATION sheet PCT/ISA/210

3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box II Observations where unity of invention is lacking (Continuation of item 2 of first sheet)

This International Searching Authority found multiple inventions in this International application, as follows:

1. ☐ As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims.

2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.

3. ☐ As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:

4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest.
- ☐ No protest accompanied the payment of additional search fees.

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

Continuation of Box I.2

Claims Nos.: 3-5 11 12

Claims 3-5, 11 and 12 all includes a "valley" as the essential technical feature. A valley is at first hand an element in a landscape and not a precise technical term. It is, however, known that "valley" have been used as a methaphor for a minimum in the progression of a curve. Such a minimum may be assumed in the present context.

"...the drain voltage..." is not a well defined term as a voltage (tension) is measured between two points. Return/ground may be chosen as the other reference point, but another reference point may change a minimum into a maximum.

As a choice of the applicant, none of the figures show any detailed voltage-curves though such voltage-curves would be essential in understanding the control based on "valleys" in the voltage. The "Sp" and "Ss" - curves appear to be no more than indicators of the ON/OFF-state of the switches; at least no "valleys" are to be seen.

The text in three cases mentions the "valleys": "...one of the later valleys..." (page 7, line 33), "...indicating the presence of a valley in the switch voltage..." (page 8, line 23) and "...valley of the drain voltage could be controlled..." (page 9, lines 20-32).

In one case more valleys are stated to be present, in another case the valley is a phenomenon that can be observed in the switch voltage and in a third case the valley is a phenomenon that is actively controlled.

Neither the claims, nor the description provides the man skilled in the art with a clear and unambiguous idea of the nature of these "valleys" in the drain voltage.

Though parasitic capacitance could be involved (page 5, line 28) the operation is based on ideal-assumptions (page 6, line 3). The description and the figures relating to the operation of the bidirectional flyback converter match this ideal-assumption, and no "valleys" are introduced under ideal operation. The minimum value of the drain voltage is when the primary switch is ON (eventually when its intrinsic diode is ON) and no apparent valleys are present.

The valley-issue MAY arise from non-ideal operation that leaves certain resonance between the transformer inductance and the parasitic capacitance. The exact result of such resonance is not obvious and control based on such phenomena is for sure not obvious.

As neither the description and nor the claims provide any clear picture of the nature of "valley"-control, and no figure is disclosing the phenomenon, no search of this is possible.

The applicant's attention is drawn to the fact that claims, or parts of claims, relating to inventions in respect of which no international search report has been established need not be the subject of an international preliminary examination (Rule 66.1(e) PCT). The applicant is advised that the EPO policy when acting as an International Preliminary Examining Authority is normally not to carry out a preliminary examination on matter which has not been searched. This is the case irrespective of whether or not the claims are amended following receipt of the search report or during any Chapter II procedure.

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/JP 02/02364

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 6084784	A	04-07-2000	DE 19828038 A1 EP 0967714 A2 JP 2000037074 A KR 2000006319 A TW 437146 B	30-12-1999 29-12-1999 02-02-2000 25-01-2000 28-05-2001
US 5745351	A	28-04-1998	FR 2729516 A1 EP 0722211 A1 JP 8275513 A	19-07-1996 17-07-1996 18-10-1996
AT 399432	B	26-05-1995	AT 175791 A	15-09-1994
US 5220492	A	15-06-1993	AT 125080 T CA 2014612 A1 DE 69020873 D1 DE 69020873 T2 EP 0434889 A2 JP 3215174 A	15-07-1995 26-06-1991 17-08-1995 04-04-1996 03-07-1991 20-09-1991
US 5109326	A	28-04-1992	NONE	